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From: Abdul Zindani  
Texas Instruments Incorporated  
Facsimile: 972-917-4418  
Phone: 972-917-5137

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Bhupendra Sharma, et al.

Docket No.: TI-38242

Serial No.: 10/711,532

Art Unit: 2816

Filed: September 24, 2004

Examiner: Anh Quan Tra

Customer No. 23494

Conf. No.: 5531

For: Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to  
Undesirable Supply-Sequencing in Environments With Higher Supply Voltages

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**FACSIMILE COVER SHEET**

<input checked="" type="checkbox"/> <b>FACSIMILE COVER SHEET</b>	<input type="checkbox"/> <b>AMENDMENT</b> <u>III</u> (9 Pages)
<input type="checkbox"/> <b>NEW APPLICATION</b>	<input type="checkbox"/> <b>EOT</b> _____ (# Page)
<input type="checkbox"/> <b>DECLARATION (# Pages)</b>	<input type="checkbox"/> <b>NOTICE OF APPEAL</b> (# Pages)
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<input type="checkbox"/> <b>DIVISIONAL APPN</b>	
NAME OF INVENTOR(S):  Bhupendra Sharma, et al.	
TITLE OF INVENTION: Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to Undesirable etc.	
TI FILE NO.:  <b>TI-38242</b>	DEPOSIT ACCT. NO.:  <b>20-0668</b>
FAXED:  <b>1/5/2006</b>	
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PO Box 655474, M/S 3999  
Dallas, TX 75265

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT PURSUANT TO 37 CFR 1.111

Dear Sir:

This amendment is in response to the Office Action dated October 13, 2005, having a statutory period for response set to expire on January 13, 2006.

Amendments to Claims begin on page 2.

Remarks/Arguments begin on page 6.

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